Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S2	11	(US-4604692-$ or US-4827408-$ or US-5402014-$ or US-5414380-$ or US-5428748-$ or US-5561826-$ or US-5812867-$ or US-5918068-$ or US-6029155-$ or US-6044428-$ or US-6216191-$).did.	USPAT	OR	OFF	2003/07/31 09:10
S5	271	configurable with (peripheral UART serial adj communication)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/07/31 12:54
S4	29	configurable with UART	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/07/31 12:54
S6	30	configurable with UART	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/07/31 13:09
S7	21	(("4006457") or ("4071887") or ("4156932") or ("4187394") or ("4509113") or ("4682304") or ("4823312") or ("4841440") or ("4843389") or ("4930065") or ("4964124") or ("4982325") or ("5063494") or ("5134691") or ("5179661") or ("5793999") or ("5805922") or ("5907719") or ("5991546") or ("6035344") or ("6256687")).PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2003/07/31 16:25
S12	40	single near3 (IC or (integrated adj2 (device chip) chip)) with UART	USPAT	OR	ON	2003/07/31 16:27
S11	45	single near3 (IC or (integrated adj2 (device chip) chip)) with UART	US-PGPUB; USPAT	OR	ON	2003/07/31 16:27
S10	0	(single near3 (IC or (integrated adj2 (device chip) chip)) with UART) same configur$3	US-PGPUB; USPAT	OR	ON	2003/07/31 16:27
S9	0	(single near3 (IC or (integrated adj2 (device chip) chip)) with UART) same configurable	US-PGPUB; USPAT	OR	ON	2003/07/31 16:27
S8	0	(single near3 (IC or (integrated adj2 (device chip) chip)) with UART) same configurabel	US-PGPUB; USPAT	OR	ON	2003/07/31 16:27

S3	271	configurable with (peripheral UART serial adj communication)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/07/31 16:39
S15	1	(configurable with (peripheral UART serial adj communication)) and (fixed single one) near2 baud adj rate	USPAT	OR	ON	2003/07/31 16:41
S14	1	(configurable with (peripheral UART serial adj communication)) and (fixed single one) near2 baud adj rate	USPAT	OR	OFF	2003/07/31 16:41
S16	179	configurable with (peripheral UART serial adj communication)	USPAT	OR	OFF	2003/07/31 16:42
S13	179	configurable with (peripheral UART serial adj communication)	USPAT	OR	OFF	2003/07/31 16:42
S17	30	(configurable with (peripheral UART serial adj communication)) and baud adj rate	US-PGPUB; USPAT	OR	ON	2003/07/31 18:01
S19	0	peripheral adj devices with UART with flash adj memory	US-PGPUB; USPAT	OR	ON	2003/07/31 18:14
S18	10	(("4796234") or ("5193199") or ("5220673") or ("5257380") or ("5307464") or ("5426744") or ("5455923") or ("5950222") or ("6145020") or ("6272584")).PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2003/07/31 18:14
S20	38	peripheral adj devices with UART with memory	US-PGPUB; USPAT	OR	ON	2003/07/31 18:50
S22	303	UART with baud adj rate	USPAT	OR	ON	2003/07/31 18:51
S21	0	UART with one adj baud adj rate	USPAT	OR	ON	2003/07/31 18:51
S24	1	("5307464").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2003/07/31 19:25
S23	62	(UART with baud adj rate) with (one single limited)	USPAT	OR	ON	2003/07/31 19:25
S25	1	(("5307464").PN.) and peripheral	US-PGPUB; USPAT	OR	ON	2003/07/31 19:26
S26	1	("5307464").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2004/01/22 18:10
S29	0	FPGA.ti. with UART	US-PGPUB; USPAT	OR	ON	2004/01/22 18:11
S28	264	FPGA.ti.	US-PGPUB; USPAT	OR	ON	2004/01/22 18:11
S27	0	(("5307464").PN.) and FPGA	US-PGPUB; USPAT	OR	ON	2004/01/22 18:11

S31	28	(RAM ROM) with FPGA.ab.	US-PGPUB; USPAT	OR	ON	2004/01/22 18:12
S30	1263	(RAM ROM) with FPGA	US-PGPUB; USPAT	OR	ON	2004/01/22 18:12
S32	1	("6405268").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2004/01/28 14:20
S34	110	configurable near3 peripheral	US-PGPUB; USPAT	OR	ON	2004/12/14 07:05
S38	76	S34 and @ad<"20010322"	US-PGPUB; USPAT	OR	ON	2004/12/14 07:07
S39	12	("5574927" | "5721855" | "5796981" | "5799169" | "5802359" | "5832251" | "5968161" | "6081888" | "6134707" | "6272453" | "6341345" | "6438679").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2004/12/14 07:15
S41	1	S40 and " 63"	US-PGPUB; USPAT	OR	ON	2004/12/14 12:26
S40	1	("5307464").PN.	US-PGPUB; USPAT	OR	OFF	2004/12/14 12:26